

**INFORMATION DISCLOSURE
STATEMENT BY APPLICANT**

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First Named Inventor	Yoshio TERADA
Art Unit	1796
Examiner Name	Lorna M. Douyon
Attorney Docket Number	Q81096

U.S. PATENTS

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U.S. PATENT APPLICATION PUBLICATIONS

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FOREIGN PATENT DOCUMENTS

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/LMD/	1	11-054487	JP	A	1999-02-26	MATSUSHITA ELECTRIC IND CO LTD		English language abstract & machine generated English translation
/LMD/	2	2004-146700	JP	A	2004-05-20	KYOCERA CORP		English language abstract & machine generated English translation
/LMD/	3	2002-113435	JP	A	2002-04-16	NITTO DENKO CORP		English language abstract & machine generated English translation

NON-PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city, and/or country where published.	T ⁵
/LMD/	1	Japanese Notification of Reasons for Refusal dated July 7, 2009	Y

EXAMINER SIGNATURE

Examiner Signature	Date Considered
/Lorna M. Douyon/	12/07/2009

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